

# Energy-Aware System Design: Algorithms And Architectures



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View Guilherme Corr a's where I work on video coding algorithms and digital systems design. Development of algorithms and hardware architectures for

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(2008) ETAHM: an energy-aware task allocation algorithm for multiprocessor systems. In: Design, task scheduling onto heterogeneous MPSoC architectures

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Track Descriptions. Home. Green architecture, strategies, algorithms, protocols, scheduling, Energy-aware system design;

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Special Issue on Real-Time Energy-Aware Circuits and Systems for including not only architectures but also algorithms and and energy-aware HEVC codec design

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about the High Efficiency Video Coding (HEVC) include energy-aware signal processing algorithms and low-power circuit and system design for portable

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Battery modeling for energy aware system design (2003) by R Rao, S Vrudhula which provide important inputs required for algorithm and system design (e.g.,

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recent works have concentrated on developing energy-aware scheduling algorithms. Transactions on Computer-Aided Design of Integrated Circuits and Systems,

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